

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Tsukasa OOISHI

Serial No.: 09/986,583

Filed: November 09, 2001

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE CAPABLE OF SELF-  
TESTING INTERNAL POWER SUPPLY CURRENTS PROVIDED TO INTERNAL  
CIRCUITS INTEGRATED ON CHIP



: Customer Number: 20277

: Confirmation Number: 5822

: Group Art Unit: 2829

: Examiner: Emily Chan

STATUS INQUIRY

Mail Stop Status Inquiry  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

The above-identified application was filed on November 9, 2001. Since more than one and one-half years have elapsed since that date, and no action has been received by attorney for applicant, Applicant respectfully requests that the undersigned attorney be informed of the status of the referenced application. It is noted that the Official Gazette indicates applications filed on or before May 5, 2002 in the above group art unit should have received an action on the merits.

Respectfully submitted,

MCDERMOTT, WILL & EMERY

A handwritten signature in cursive script, reading "Gene Z. Robinson".

Gene Z. Robinson  
Registration No. 33,351

600 13<sup>th</sup> Street, N.W.  
Washington, DC 20005-3096  
(202) 756-8000 GZR:cms  
Facsimile: (202) 756-8087  
**Date: August 8, 2003**